



JPW

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Hiroyuki TOMIMATSU

Group Art Unit: 2814

Application No.: 10/782,810

Examiner: H. Pham

Filed: February 23, 2004

Docket No.: 114371.01

For: SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE
SAME, CIRCUIT SUBSTRATE AND ELECTRONIC APPARATUS

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In reply to the May 13, 2005 Restriction Requirement, Applicant provisionally elects
Group I, claims 1-13.

Prompt examination and allowance of the application are earnestly solicited.

Respectfully submitted,

James A. Oliff
Registration No. 27,075

Gang Luo
Registration No. 50,559

JAO:GXL/sqb
Date: June 13, 2005

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